

# A-SSCC 2021 Program Schedule At-a-glance

Date	Time	Crystal Ballroom 1	Crystal Ballroom 2	Crystal Ballroom 3	Pearl Room		
Sunday Nov. 7th 2021	08:30~			On-Site Registration (3F Lobby)			VIP Room
	09:00-10:30 (90')			<b>Tutorial 1</b> Incremental Delta-Sigma ADCs: Past, Present, and Future <b>Youngcheol Chae</b> Yonsei University, Korea			
	10:30-10:50 (20')			Break Time			
	10:50-12:20 (90')			<b>Tutorial 2</b> Mm-Wave PA Designs – Fundamentals, Architectures, and The State of the Art <b>Hua Wang</b> ETH Zürich, Switzerland			
	12:20-14:00 (100')			Lunch Break			
	14:00-15:30 (90')			<b>Tutorial 3</b> Always-On Systems for Next-Gen IoT – From Less Battery to Battery-Less <b>Massimo Alioto</b> National University of Singapore			
	15:30-15:50 (20')			Break Time			
	15:50-17:20 (90')			<b>Tutorial 4</b> Fundamentals of NAND Flash Memory <b>Chi-Weon Yoon</b> Samsung Electronics, Korea			
	18:00-19:30 (90')			Welcome Reception			
	19:30-20:30 (60')			TPC Meeting			

Date	Time	Crystal Ballroom 1	Crystal Ballroom 2	Crystal Ballroom 3	Pearl Room	Grand Ballroom Lobby			
Monday Nov. 8th 2021	08:15 ~	Registration Open (3F Lobby)						VIP Room	
	08:30-08:50 (20')	Opening Ceremony / Welcome Speech (Crystal Ballroom)							
	08:50-09:35 (45')	<b>Plenary Speech 1 (Crystal Ballroom)</b> Creating the Future with Silicon in the Smart & Connected World <b>ES Jung</b> , President & Chief Technology Officer, Samsung Electronics Device Solutions							
	09:35-10:20 (45')	<b>Plenary Speech 2 (Crystal Ballroom)</b> Zero Trust Security for Intelligent Electronics <b>Charles Ching-Hsiang Hsu</b> , Chairman, eMemory Technology Inc, Chairman, PUFSecurity Cooperation							
	10:20-10:40 (20')	Break							
	10:40-12:40 (120')	<b>Session 1 (A1L-A)</b> (Industry Program) Advanced Design, Reliability & Integration Techniques	<b>IT Vision in Asia</b> "Beyond 5G, paving a path toward 6G"						
	12:40-14:00 (80')	Lunch Break							
	14:00-15:40 (100')	<b>Session 2 (A2L-A)</b> Power Conversion & Wireless Power Transfer	<b>Session 3 (A2L-B)</b> AI & Vision Processors	<b>Session 4 (A2L-C)</b> Terahertz & Millimeter-Wave Circuits & Systems	<b>Session 5 (A2L-D)</b> Circuits & Systems for Emerging Applications		Industry Demo & SDC Exhibition & FPGA Demo (15:40-19:30)		
	15:40-16:00 (20')	Break							
	16:00-17:40 (100')	Panel Discussion (Crystal Ballroom)							
	17:40-17:50 (10')	Break							
	17:50-19:30 (100')	Social Event							

Date	Time	Crystal Ballroom 1	Crystal Ballroom 2	Crystal Ballroom 3	Pearl Room				
Tuesday Nov. 9th 2021	08:30 ~	Registration Open (3F Lobby)						VIP Room	
	09:00-09:45 (45')	<b>Plenary Speech 3 (Crystal Ballroom)</b> From Labs to Startups: Reinventing Engineering Education <b>Zexiang Li</b> , Professor, HKUST							
	09:45-10:30 (45')	<b>Plenary Speech 4 (Crystal Ballroom)</b> Electronics of the next generation aim to harmonize with humans <b>Takao Someya</b> , Dean, School of Engineering, the University of Tokyo							
	10:30-10:50 (20')	Break							
	10:50-12:30 (100')	<b>Session 6 (B1L-A)</b> Temperature & Voltage Managements	<b>Session 7 (B1L-B)</b> FPGA Design for AI/ML Applications	<b>Session 8 (B1L-C)</b> Smart Memory	<b>Session 9 (B1L-D)</b> Clock Recovery & Generation				
	12:30-14:00 (90')	Lunch Break							
	14:00-15:40 (100')	<b>Session 10 (B2L-A)</b> SAR-based Hybrid ADCs & TDC	<b>Session 11 (B2L-B)</b> Security & Signal Processing SoCs	<b>Session 12 (B2L-C)</b> High-Performance VCOs & PLLs	<b>Session 13 (B2L-D)</b> Imagers & ToFs		Industry Demo & SDC Exhibition & FPGA Demo (14:00-18:00)		
	15:40-16:00 (20')	Break							
	16:00-18:00 (120')		<b>ESSCIRC Joint Session</b>	<b>Industry Forum</b>					
	18:00-18:30 (30')	Break							
	18:30-20:30 (120')	Banquet							

Date	Time	Crystal Ballroom 1	Crystal Ballroom 2	Crystal Ballroom 3	Pearl Room			
Wednesday Nov. 10th 2021	08:30 ~	Registration Open (3F Lobby)						VIP Room
	09:00-10:40 (100')	<b>Session 14 (C1L-A)</b> Performance Enhancement Techniques for Oversampled ADCs	<b>Session 15 (C1L-B)</b> Secure & Resilient Circuits	<b>Session 16 (C1L-C)</b> High-Power & Low-Power RF Techniques	<b>Session 17 (C1L-D)</b> High Performance Memory			
	10:40-11:00 (20')	Break						
	11:00-12:40 (100')	<b>Convergence Workshop</b>	<b>Learn to Establish and Achieve Professionalism (LEAP) Workshop</b>					
	12:30-14:00 (90')	Lunch Break						
	14:00-15:40 (100')	<b>Session 18 (C2L-A)</b> Clock Generation & Envelope Tracking	<b>Session 19 (C2L-B)</b> FPGA Design for Emerging Applications	<b>Session 20 (C2L-C)</b> Emerging Circuits & Systems for Biomedical Applications	<b>Session 21 (C2L-D)</b> Wireline Transceiver Techniques			
	15:40-16:00 (20')	Break						
	16:00-18:00 (120')	<b>Latest New Tech. &amp; Hot Issue Forum</b>	<b>Rising Star Express (RISE) Forum</b>					